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SUMMARY

Electrical and Computer Engineering graduate student with strong technical and engineering skills in RF and Wireless Engineering. Possesses the analytical abilities necessary for designing, developing and testing RF circuits. Gained exposure to key concepts in modern wireless communications by training with major telecom company.

EDUCATION

University of Illinois at Chicago (UIC) - Chicago, IL Master of Science in Electrical and Computer Engineering

Vellore Institute of Technology (VIT) - Vellore, India Bachelor of Science in Electronic and Communication Engineering

TECHNICAL SKILLS

Hardware: Amplifiers, Mixers, RF Network Analyzers, RF Filters- SAW, BAW, Spectrum analyzers, Oscilloscopes, Resonant circuits, RF oscillators: PLL, Voltage-controlled oscillator, Transmitters.

Software: MATLAB, Simulink, LTSpice, AWR, Labview, FEKO, Altera Quartus, Atalanta (ATPG Tool), Microsoft Word, Excel, PowerPoint, C, C++ and Linux.

LABORATORY WORK

RF and Microwave Lab, Electrical and Computer Engineering Department, UIC Research Assistant

- Conducted experiments using waveguide tees and directional couplers, impedance matching using smith charts, ٠ Frequency synthesizer characteristics measurement, S-Parameters and Slotted line measurements.
- Measurements were performed using FieldFox handheld RF and Microwave analyzer.

INTERNSHIP EXPERIENCE

Bharat Sanchar Nigam Limited (BSNL) - Hyderabad Area, India

Engineering Intern

- Obtained knowledge on key wireless concepts such as: Digital Switching Principles (PCM Principles, CAS, CCS7 and latest switches in telecom industry), Fiber Optic Communication Principle (Concepts on SDH and DWDM), Mobile communication Principles (GSM, GPRS, EDGE, CDMA, 3G Technologies).
- Gained practical exposure on latest equipment's in telecom such as: Telecom Switch- CDOT, OF Systems- SDH, • DWDM, Mobile Equipment- 2G GSM, CDMA, 3G Mobile, Broadband, Networking Equipment, OFC Station, GSM/CDMA Installations.

ACADEMIC PROJECTS

Microstrip impedance matching circuit

Designed and simulated a circuit using FEKO by impedance matching on a micro strip line using single stub tuning method with the help of Smith charts; results were successfully obtained.

Impedance matching circuit using FEKO

Matched microstrip line using single stub tuning method. Performed impedance matching using smith chart. Designed, developed and simulated microstrip line using FEKO software and obtained successful results.

Low-pass elliptical filters by cascaded microstrip rectangular elements

- Designed low-pass elliptical filters with cascaded microstrip rectangular elements using AWR Microwave Office. •
- Demonstrated that pass band attenuation even lower than that of the prototype can be achieved through a simple adjusting procedure for compensating the reactance due to higher order resonant modes.

ACHIEVEMENTS

- 1st prize in ECE technical quiz in inter-school quiz competition at Audishankara engineering college
- Placed in top 10 students for ECE RF engineering competition sponsored by IAETSD

May 2019 (Expected) GPA: 3.63/4.0

> May 2015 GPA: 3.7/4.0

Feb. 2016 – Present

Oct. 2015 – Dec. 2015

Feb. – May 2018

Sept. - Oct. 2018

Sept. – Dec. 2017